



1uF (or 2.2uF) capacitor from the LDOOUT/CLDO to the VSS is required (close to MCU).

External pull up resistance of the nRST can be changed to 10K for interference/noise rejection. The capacitor from the nRST to VSS can be NC if no button used.

The VBAT must connect to the VDD when no extra battery is used in the system (VBAT floating is not allowed).

The BOOT pin must be maintained at high level when reset. 4.7k external pull up resistance may be required for the BOOT pin (for interference/noise rejection and rising time during the reset).

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