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IAP 说明

IAP 功能使用户可以方便地对 Flash 程序存储器进行多次编程。IAP 功能可以通过内部固件进行程序的更新，而无需外接烧录器或 PC。

example 说明

此范例演示了 **IAP 的读写操作** 的使用，对MCU的最后一个 page 写擦除，然后依次写入0~127，并利用 **IAP_Read** 读回

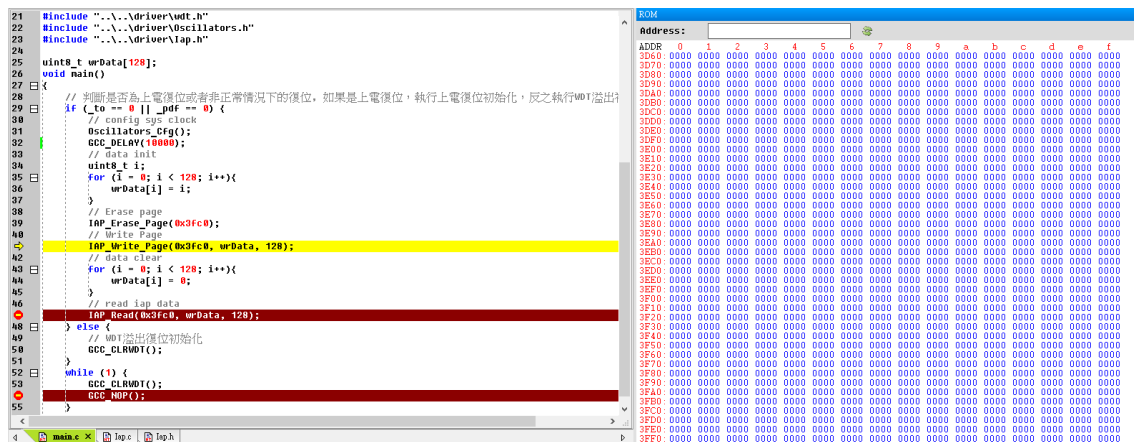
1. 连接e-link与开发板
2. 编译并下载程序
3. 单步执行(通过IDE3000查看数据)

现象

下面以 **BH66F2663** 的范例说明，其他类似，只是ROM地址不一样

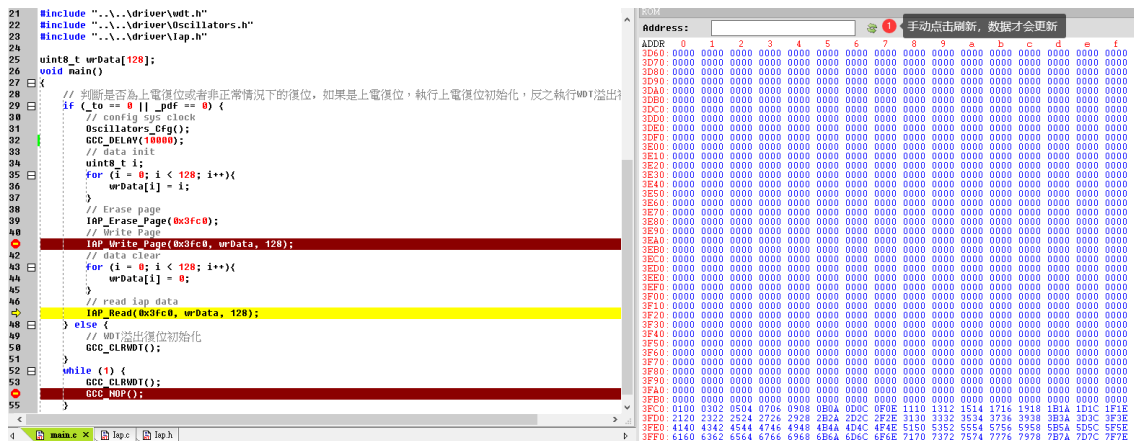
1. 程序执行到 **IAP_Write_Page**，查看对应地址ROM区域(要手动刷新，数据才会更新)，全部清除为

0



2. 程序执行到 **IAP_Read**，查看对应地址ROM区域(要手动刷新，数据才会更新)，依次写入

0x00~0x7F



3. 程序执行到 **GCC_NOP**，在监视器窗口查看wrData，会发现数据读出

Name		Value	Address	Space
wrData[0]	{...}	0x0000	0x0180	Ram
wrData[1]	0x00	0x0000	0x0181	Ram
wrData[2]	0x00	0x0000	0x0182	Ram
wrData[3]	0x00	0x0000	0x0183	Ram
wrData[4]	0x00	0x0000	0x0184	Ram
wrData[5]	0x00	0x0000	0x0185	Ram
wrData[6]	0x00	0x0000	0x0186	Ram
wrData[7]	0x00	0x0000	0x0187	Ram
wrData[8]	0x00	0x0000	0x0188	Ram
wrData[9]	0x00	0x0000	0x0189	Ram
wrData[10]	0x00	0x0000	0x018a	Ram
wrData[11]	0x00	0x0000	0x018b	Ram
wrData[12]	0x00	0x0000	0x018c	Ram
wrData[13]	0x00	0x0000	0x018d	Ram
wrData[14]	0x00	0x0000	0x018e	Ram
wrData[15]	0x00	0x0000	0x018f	Ram
wrData[16]	0x00	0x0000	0x0190	Ram
wrData[17]	0x00	0x0000	0x0191	Ram
wrData[18]	0x00	0x0000	0x0192	Ram
wrData[19]	0x00	0x0000	0x0193	Ram
wrData[20]	0x00	0x0000	0x0194	Ram
wrData[21]	0x00	0x0000	0x0195	Ram
wrData[22]	0x00	0x0000	0x0196	Ram
wrData[23]	0x00	0x0000	0x0197	Ram
wrData[24]	0x00	0x0000	0x0198	Ram
wrData[25]	0x00	0x0000	0x0199	Ram
wrData[26]	0x00	0x0000	0x019a	Ram
wrData[27]	0x00	0x0000	0x019b	Ram
wrData[28]	0x00	0x0000	0x019c	Ram
wrData[29]	0x00	0x0000	0x019d	Ram
wrData[30]	0x00	0x0000	0x019e	Ram
wrData[31]	0x00	0x0000	0x019f	Ram
wrData[32]	0x00	0x0000	0x01a0	Ram
wrData[33]	0x00	0x0000	0x01a1	Ram
wrData[34]	0x00	0x0000	0x01a2	Ram
wrData[35]	0x00	0x0000	0x01a3	Ram
wrData[36]	0x00	0x0000	0x01a4	Ram
wrData[37]	0x00	0x0000	0x01a5	Ram
wrData[38]	0x00	0x0000	0x01a6	Ram
wrData[39]	0x00	0x0000	0x01a7	Ram
wrData[40]	0x00	0x0000	0x01a8	Ram
wrData[41]	0x00	0x0000	0x01a9	Ram
wrData[42]	0x00	0x0000	0x01aa	Ram
wrData[43]	0x00	0x0000	0x01ab	Ram
wrData[44]	0x00	0x0000	0x01ac	Ram
wrData[45]	0x00	0x0000	0x01ad	Ram
wrData[46]	0x00	0x0000	0x01ae	Ram

FAQ

1. 为什么需要delay

- 确保 fsub 时钟在执行擦或写动作前已稳定。
- IAP读写时钟来自fsub，为防止fsub还没稳定，通常不建议上电立即执行IAP读写操作，此处为方便演示，采用上电delay后在读写IAP